

Notice of References Cited	Application/Control No. 09/932,332	Applicant(s)/Patent Under Reexamination GOLDSMITH ET AL.	
	Examiner CHAMELI C. DAS	Art Unit 2192	Page 1 of 1

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	C	US-6,820,251	11-2004	Dwyer, Lawrence	717/130
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	TITLE: Design verification via simulation and automatic test pattern generation, author: Al-Assad et al, IEEE, 1995.
	V	TITLE: Modeling and Testing a Critical Fault-Tolerant Multi-Process System, author: Riter, IEEE, 1995.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.